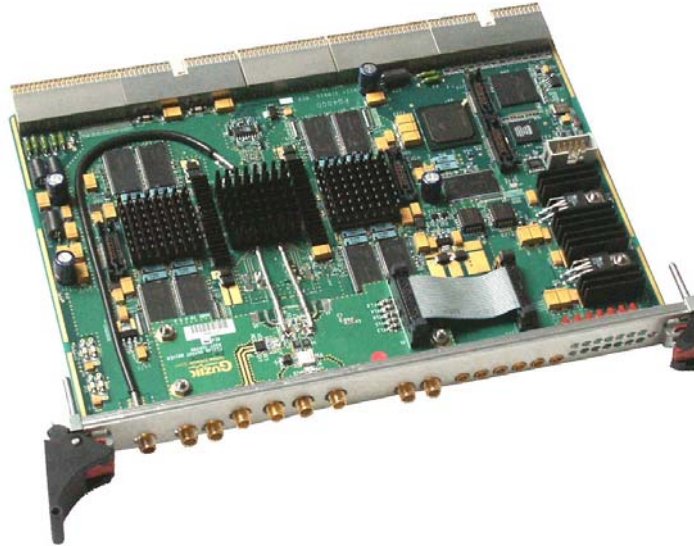


GUZIK PRODUCT BULLETIN

Pattern Generator PG-5000



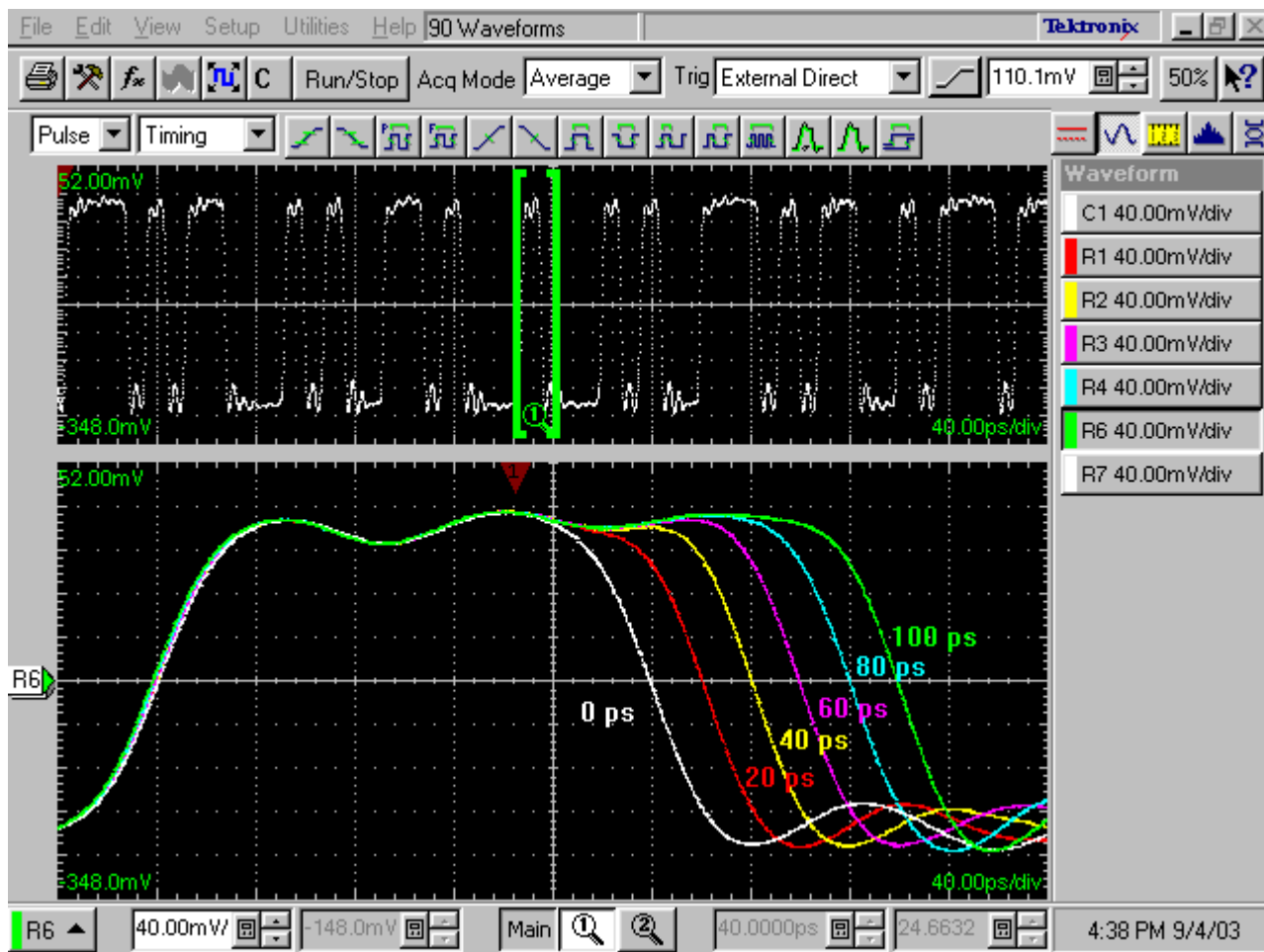
Preliminary

- **Three channels, each with 5 Gs/sec maximum data rate**
- **200 psec minimum bit cell**
- **Main channel with individual transition precompensation:
250 fsec precompensation resolution at 5 Gs/sec
Any transition in pattern can be precompensated separately**
- **Less than 2 psec (RMS) random jitter**
- **± 5 psec pattern dependant jitter (pk-pk, typical)**
- **Up to 180 Mbit random pattern**
- **Output signal:
Differential CML
300mV (pk-pk) signal level, 50 Ohm termination
Rise/fall time 50 psec (typical)**
- **Single CPCI module**

NOTE: The pattern generator works together with the reference clock generator (also a single CPCI module).

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Pattern Generator Transition Precompensation (Timing Shift) with 20 psec Steps

