



## Release Notes

# **WITE32**

**Version 4.08**

**10/05/2009**

# CHAPTER 1

## NEW FEATURES INTRODUCED IN WITE32

The release 4.08 of WITE32 incorporates new features and bug fixes introduced after the release version 4.07. This document uses the release notes for WITE32 version 4.07 as a baseline for comparison.

### 1.1 New V2002 Tooling Types

The following cartridge and media sets for V2002 spinstand are supported for the first time in WITE32 version 4.08:

<i>Cartridge Part Number</i>	<i>Media Size</i>
80-704830/704831	2.5"
80-704860/704861	1.8"
80-705033/705034	2.5"
80-705033/705034	1.8"
80-705065/705066	3.5"
84-800717	2.5"
84-800727	3.5"
84-800737	65 mm
84-800747	2.5"

### 1.2 New Headamplifiers and Headstack

<i>New Headamplifiers</i>		
SR3424	PA2750	SR3482ABA_M
PA2975	SR3485	

Headstack HSA\_SR3780ABA, driver revision 4.06 included in WITE32 version 4.08, supports ten heads instead of eight.

### 1.3 New Utility to Check Headamplifier Driver Versions

A GUI utility to look-up versions of headamplifier drivers is added to WITE32 installation. The utility is called *HdaVer.exe*. To use it, run the *HdaVer.exe*, press the *Open* button, and select the needed *.hde* or *.dll*

file. The revisions of all drivers in the file will be listed in the output textbox. Alternatively, the utility can be run from the command line: HdaVer.exe <Filename>.

#### **1.4 Guzik Patent Info in WITE32**

The list of US and Japan patents granted to Guzik Technical Enterprises is displayed during WITE32, WDK32, WDM Toolkit, and D5000 Signal Explorer installation. It also is displayed in the *About* dialog box in WITE32 and WDCP2002.

## CHAPTER 2 FIXED BUGS

The following bugs were discovered in WITE32 version 4.07 or earlier, and fixed in WITE32 version 4.08. The description of a bug explains the bug behavior as it appears in WITE32 version 4.07.

1. *Servo Calibration* or *Write Servo* fail for low RPM and small number of sectors, for example: 2633 RPM and 64 sectors.
2. MR7/UP10 - write current resets to some low value when device starts and the *Control | Headamplifier* dialog box is open.
3. Two issues in the UP10 and SR3722AAA headamplifier configuration:
  - When the *Headamplifier* dialog box is open and *Device Start* pressed, the read bias resets to 50mV.
  - *Register Access* control does not show upper 8 bits (chip has 16-bit registers).
4. SR3420 headamplifier: *Additional* TFC mode for the single-ended TFC design is controlled and measured as if it is differential, thus setting and reading wrong TFC values. The improved SR3420 driver revision is 4.08.
5. A “*Division by 0*” message may pop-up when setting zero volts for the *Additional* TFC mode. This may occur in headamplifier drivers with Guzik TFC voltage-drop compensation. The list of affected headamplifiers and headstacks: HSA\_PA2740, PA2740, PA7840M, HSA\_PA7840, PA7840, HSA\_PA7840B3, SR3480M\_C, SR3480ABA\_M, HSA\_SR3482, SR3482AAA, HSA\_SR3482ABA, SR3482ABA, SR3780AAA\_M, HSA\_SR3780ABA, SR1984CAA\_HM, SR1984BBA2\_HM, SR1984CAA\_HM\_ND, SR1984CCA\_HM, SR1984\_HM, SR1984BBA\_HM, SR3424.
6. SR3482ABA headamplifier: When setting a voltage value for the *Additional* TFC mode in WITE32, and re-opening the *Headamplifier* dialog box, the values are different, and always higher than what was set originally.