

# GUZIK PRODUCT BULLETIN

## Parametric TFC Control



- Dynamic adjustment of write-mode TFC power, depending on four parameters:
  - Write current,
  - Write current overshoot amplitude,
  - Write current overshoot duration,
  - Write signal frequency
- Works transparently with WITE32, Guzik test modules, and WDK-based custom modules
- Calculates write signal frequency depending on a data pattern, not just write clock

Parametric TFC Setup

Enable

Warn if WFTC Adjustment Positive

Awc: -0.1    Bwc: 0    Dwc: 0    Cwc,osa: 0

Aosa: -0.1    Bosa: 0    Dosa: 0    Cwc,osd: 0

Aosd: -0.1    Bosd: 0    Dosd: 0    Cwc,f: 0

Af: 0.05    Bf: 0    Df: 0    Cosa,osd: 0

Cosa,f: 0

Cosd,f: 0

Buttons: Save, Reset, Default, Zone Edit, Close

Parametric TFC Info

Headamplifier: [ ]    Refresh: [ ]

WC DAC: [ ]

OSA DAC: [ ]    WFTC: [ ]

OSD DAC: [ ]    WFTC Adjustment: [ ]

Write Signal Frequency: [ ]    WFTC Total: [ ]

$WFTC_{PARAM} (mW) = WFTC +$

$$A_0X + A_1Y + A_2Z + A_3W +$$
$$B_X(X + D_X)^2 + B_Y(Y + D_Y)^2 + B_Z(Z + D_Z)^2 + B_W(W + D_W)^2 +$$
$$C_{XY}(X + D_X)(Y + D_Y) + C_{XZ}(X + D_X)(Z + D_Z) +$$
$$C_{YW}(X + D_X)(W + D_W) + C_{YZ}(Y + D_Y)(Z + D_Z) +$$
$$C_{YW}(Y + D_Y)(W + D_W) + C_{ZW}(Z + D_Z)(W + D_W)$$

, where

X is write current DAC (wc)

Y is overshoot DAC (osa)

Z is overshoot duration DAC (osd)

W is write signal frequency in MHz (f)

Parametric TFC allows for adjusting the value of the write-mode TFC power, in milli-watts, depending on write-related parameters.

Every time any of these parameters change, the adjustment is calculated, and the total write-mode TFC,  $WFTC_{PARAM}$ , which is a sum of a fixed write TFC value and TFC adjustment, is programmed into the headamplifier. Thus, the adjustment applies to all write and erase operations without intervention from the user.

The function describing the total write-mode TFC is given below:

$$\begin{aligned} W\text{TFC}_{\text{PARAM}}(mW) = & W\text{TFC} + \\ & A_X X + A_Y Y + A_Z Z + A_W W + \\ & B_X (X + D_X)^2 + B_Y (Y + D_Y)^2 + B_Z (Z + D_Z)^2 + B_W (W + D_W)^2 + \\ & C_{XY} (X + D_X) (Y + D_Y) + C_{XZ} (X + D_X) (Z + D_Z) + \\ & C_{XW} (X + D_X) (W + D_W) + C_{YZ} (Y + D_Y) (Z + D_Z) + \\ & C_{YW} (Y + D_Y) (W + D_W) + C_{ZW} (Z + D_Z) (W + D_W) \end{aligned}$$

where

WTFC is a fixed TFC value

X is write current DAC (WC)

Y is overshoot DAC (OSA)

Z is overshoot duration DAC (OSD)

W is write signal frequency in MHz (F)

Write signal frequency  $F_{WR}$  is calculated as  $F_{WR} = F_{CLK} / (2 * R)$ , where  $F_{CLK}$  is the write clock, and R is the average number of bit cells between two consecutive flux transitions. For writing through the PRML chip  $R = 2$ .

## Hardware and Software Requirements

1. WITE32 software revision 4.20 or later
2. UP8 or UP10 universal preamplifier
3. Commercial headamplifier with a driver, which supports programming TFC in milli-watts
4. Software License. Please contact [sales@guzik.com](mailto:sales@guzik.com) to obtain a quotation for the license. Please provide the RWA EEPROM Dump with your request.



2443 Wyandotte Street  
Mountain View, CA 94043  
Phone: (650) 625-8000  
Fax: (650) 625-9325  
E-mail: [sales@guzik.com](mailto:sales@guzik.com)  
<http://www.guzik.com/>